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PATENT

Attorney Docket No.: MAGMA-00702

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sharon Zohar

Serial No.: 10/812,579

Filed: March 29, 2004

For: **REDUCED ARCHITECTURE
PROCESSING PATHS**

) Group Art Unit: 2825

) Examiner:

) **TRANSMITTAL LETTER**

) 162 N. Wolfe Road
) Sunnyvale, CA 94086
) (408) 530-9700

) Customer No.: 28960

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find an Information Disclosure Statement, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. 08-1275. **An originally executed duplicate of this transmittal is enclosed for this purpose.**

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: July 22, 2004

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

Attorneys for Applicant

CERTIFICATE OF MAILING (37 CFR§ 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450

HAVERSTOCK & OWENS LLP.

Date: 7/22/04 By: Nicole P. Owens



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) **INFORMATION DISCLOSURE
STATEMENT**

) 162 N. Wolfe Road
Sunnyvale, CA 94086
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Alexandria, VA 22313-1450

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicants have become aware of the following printed publication which may be material to the examination of this application:

- U.S. Patent No. 5,519,627;
- U.S. Patent No. 5,521,836;
- U.S. Patent No. 5,586,047;
- U.S. Patent No. 5,659,484;
- U.S. Patent No. 5,812,417;
- U.S. Patent No. 5,838,583;
- U.S. Patent No. 5,841,663;
- U.S. Patent No. 5,850,349;
- U.S. Patent No. 6,026,222;
- U.S. Patent No. 6,056,784;
- U.S. Patent No. 6,086,629;
- U.S. Patent No. 6,145,117;

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450

HAVERSTOCK & OWENS LLP

Date: 7/22/04

By: Nicole [Signature]

- U.S. Patent No. 6,163,876;
- U.S. Patent No. 6,286,128 B1;
- U.S. Patent No. 6,430,734 B1;
- U.S. Patent No. 6,434,734 B1;
- U.S. Patent No. 6,438,736 B1;
- U.S. Patent No. 6,584,605 B1;
- U.S. Patent No. 6,606,737 B1;
- U.S. Patent No. 6,687,892 B1;
- P. Ienne et al., "Practical Experiences with Standard-Cell Based Datapath Design Tools - Do We Really Need Regular Layouts?", 24.4, June 98, pp. 396-401; and
- R. Gordon et al., "An N-Bus Datapath Compiler for IC Design", Silicon Compiler Systems Corp., 1989 IEEE, pp. 23.3.1- 23.3.4.

This Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,
HAVERSTOCK & OWENS LLP

Dated: July 22, 2004

By: Jonathan O. Owens
Jonathan O. Owens
Reg. No.: 37,902

Attorneys for Applicant

FORM PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark Office

Attorney Docket No.: MAGMA-00702

Serial No.: 10/812,579

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use Separate Sheets If Necessary)

Applicant: Sharon Zohar

Filing Date: March 29, 2004

Group Art Unit: 2825

(37 CFR § 1.98(b))

U.S. PATENT DOCUMENTS

Examiner Initials		Serial / Patent Number	Issue Date	Applicant / Patentee	Class	Subclass	Filing Date
	AA	5,519,627	05/21/96	Mahmood et al.	364	488	11/15/94
	AB	5,521,836	05/28/96	Hartoog et al.	364	491	12/12/94
	AC	5,586,047	12/17/96	Imahashi	364	491	10/07/94
	AD	5,659,484	08/19/97	Bennett et al.	364	491	05/04/95
	AE	5,812,417	09/22/98	Young	364	491	06/24/96
	AF	5,838,583	11/17/98	Varadarajan et al.	364	491	04/12/96
	AG	5,841,663	11/24/98	Sharma et al.	364	490	09/14/95
	AH	5,850,349	12/15/98	Hirai et al.	364	491	11/06/95
	AI	6,026,222	02/15/00	Gupta et al.	395	500.06	12/23/97
	AJ	6,056,784	05/02/00	Stanion	716	5	10/03/97
	AK	6,086,629	07/11/00	McGettigan et al.	716	12	12/04/97
	AL	6,145,117	11/07/00	Eng	716	18	01/30/98
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	AN	6,286,128 B1	09/04/01	Pileggi et al.	716	18	06/12/98
	AO	6,430,734 B1	08/06/02	Zahar	716	12	04/15/99
	AP	6,434,734 B1	08/13/02	Zahar	716	12	04/15/99
	AQ	6,438,736 B1	08/20/02	Zahar	716	12	04/15/99
	AR	6,584,605 B1	06/24/03	Zahar	716	10	04/15/99
	AS	6,606,737 B1	08/12/03	Zahar	716	12	04/15/99
	AT	6,687,892 B1	02/03/04	Zahar	716	12	04/15/99
	AU						
	AV						
	AW						

FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AX							
	AY							
	AZ							
	BA							

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

	BB	P. lenne et al., "Practical Experiences with Standard-Cell Based Datapath Design Tools - Do We Really Need Regular Layouts?", 24.4, June 98, pp. 396-401.						
	BC	R. Gordon et al., "An N-Bus Datapath Compiler for IC Design", Silicon Compiler Systems Corp., 1989 IEEE, pp. 23.3.1- 23.3.4.						
	BD							
	BE							

Examiner:

Date Considered:

EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.